ABSTRACT OF THE DISCLOSURE

A method and circuit are disclosed for a buffer for temporarily holding packets of information. The buffer may include a first memory and a second memory for holding the

5   packets of information. The first memory may be a read-once memory in which data stored in the first memory is destroyed upon being read therefrom the first time. The second memory may be a memory in which stored data therein is not destroyed following the data being read from the second memory the

10   first time. The buffer includes at least one queue. The head-of-line packet of the at least one queue is stored in the second memory. Incoming fanout splitting packets are stored in the second memory and other incoming packets are initially stored in the first memory.